

Parallel I/O for SwissTx  
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- Introduction
- SFIO (Striped File I/O)  
software architecture
- SFIO interface
- Performance measurements
- Integration into MPI-II I/O
- Conclusion

Fig. 01.

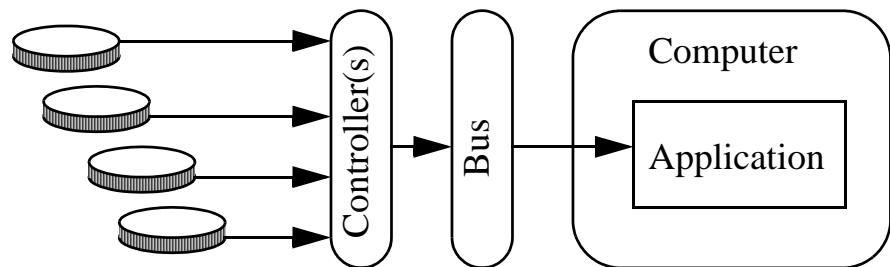
## Parallel I/O for Swiss-Tx (Introduction)

- Requirements for Parallel I/O
- Striped Files Parallel Access Solution
- Interface
- Network Communication and Disk Access optimisation

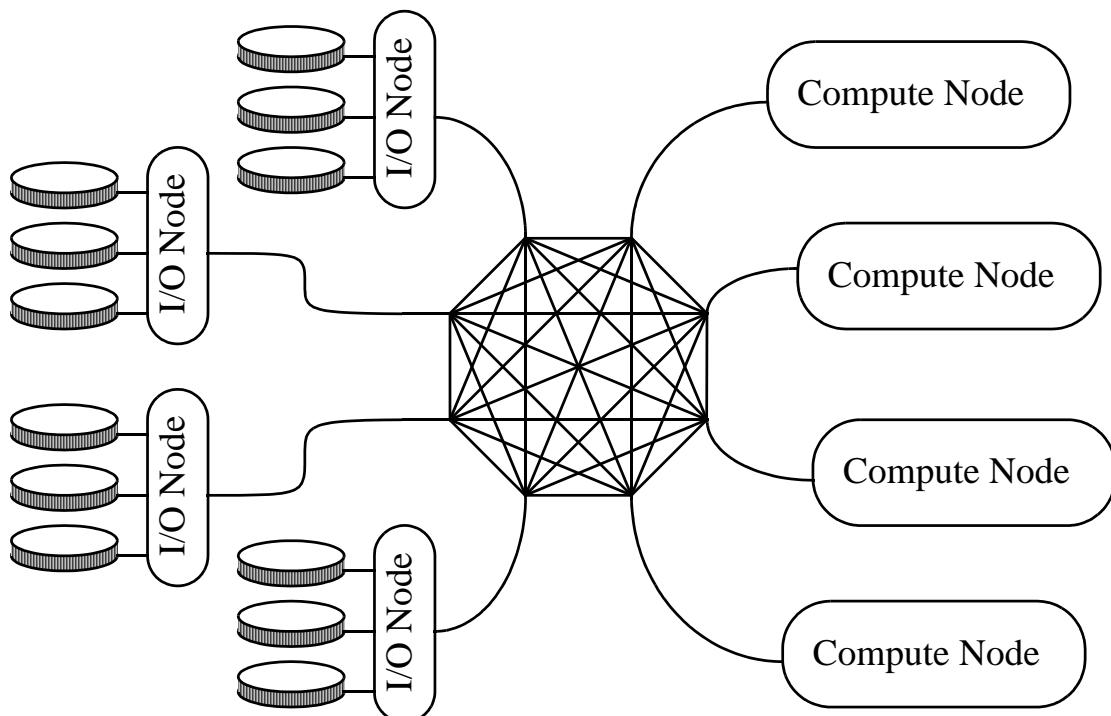
Fig. 02. We are trying to be close to the linear dependence of throughput from the

## Requirements to Parallel I/O System

- Scalable Throughput



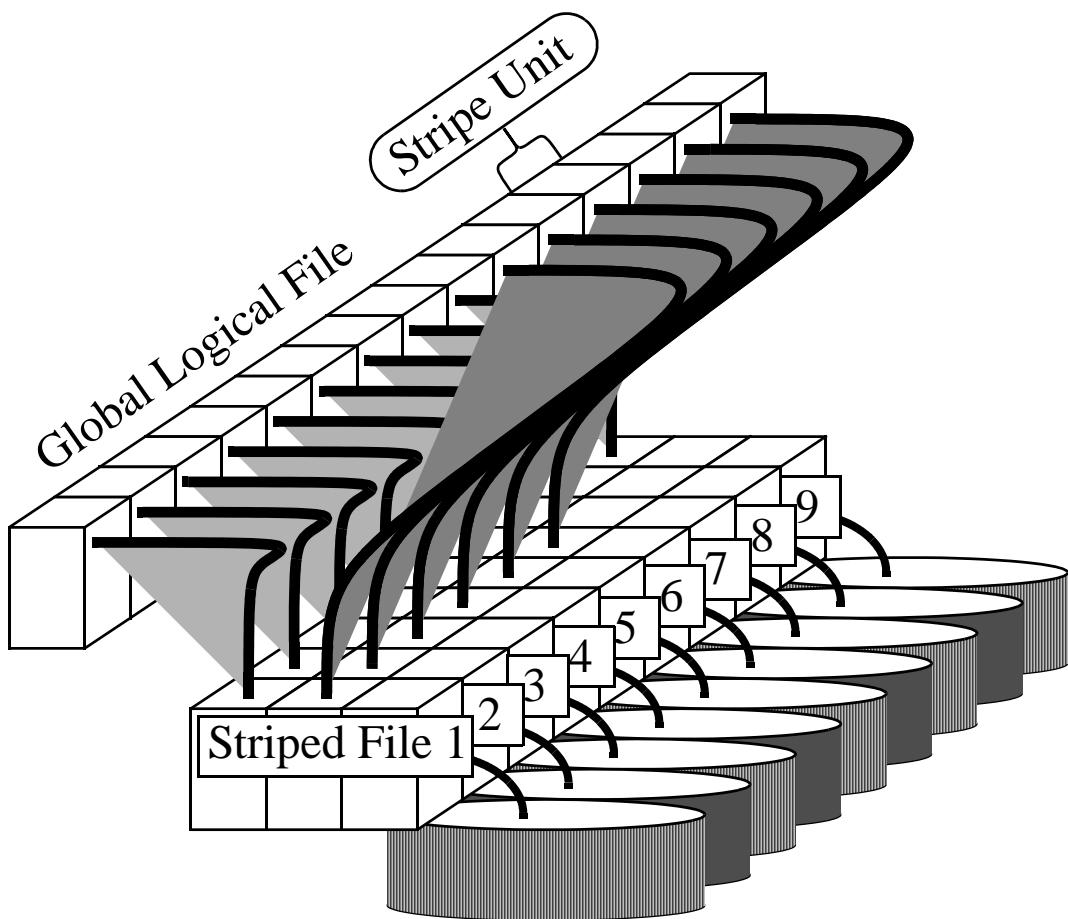
- Concurrent Access



number of I/O devices. The upper limit of throughput can be the controller(s)/bus bandwidth or the TOTAL network bandwidth.

Fig. 03. The basic idea of our implementation is cyclical distribution of logical file

## Striped Files Parallel Access Solution



across the set of striped files. Access to single part of logical file require parallel access to set of striped files.

Fig. 04. The native interface is SFIO, but we work to provide also MPI-II I/O inter-

## Interface

### ● SFIO

```
MFILE* mopen(char *s, int unitsz); // “t0-p1,/tmp/a;t0-p2,/tmp/a”
void mclose(MFILE *f);
void mchsize(MFILE *f, long size);
void mdelete(char *s);
void mcreate(char *s);
void mread(MFILE *f, long offset, char *buf, unsigned count);
void mwrite(MFILE *f, long offset, char *buf, unsigned count);
void mreadb(MFILE *f, unsigned bcount,
            long Offset[], char *Buf[], unsigned Count[]);
void mwriteb(MFILE *f, unsigned bcount,
              long Offset[], char *Buf[], unsigned Count[]);
```

### ● MPI-II I/O

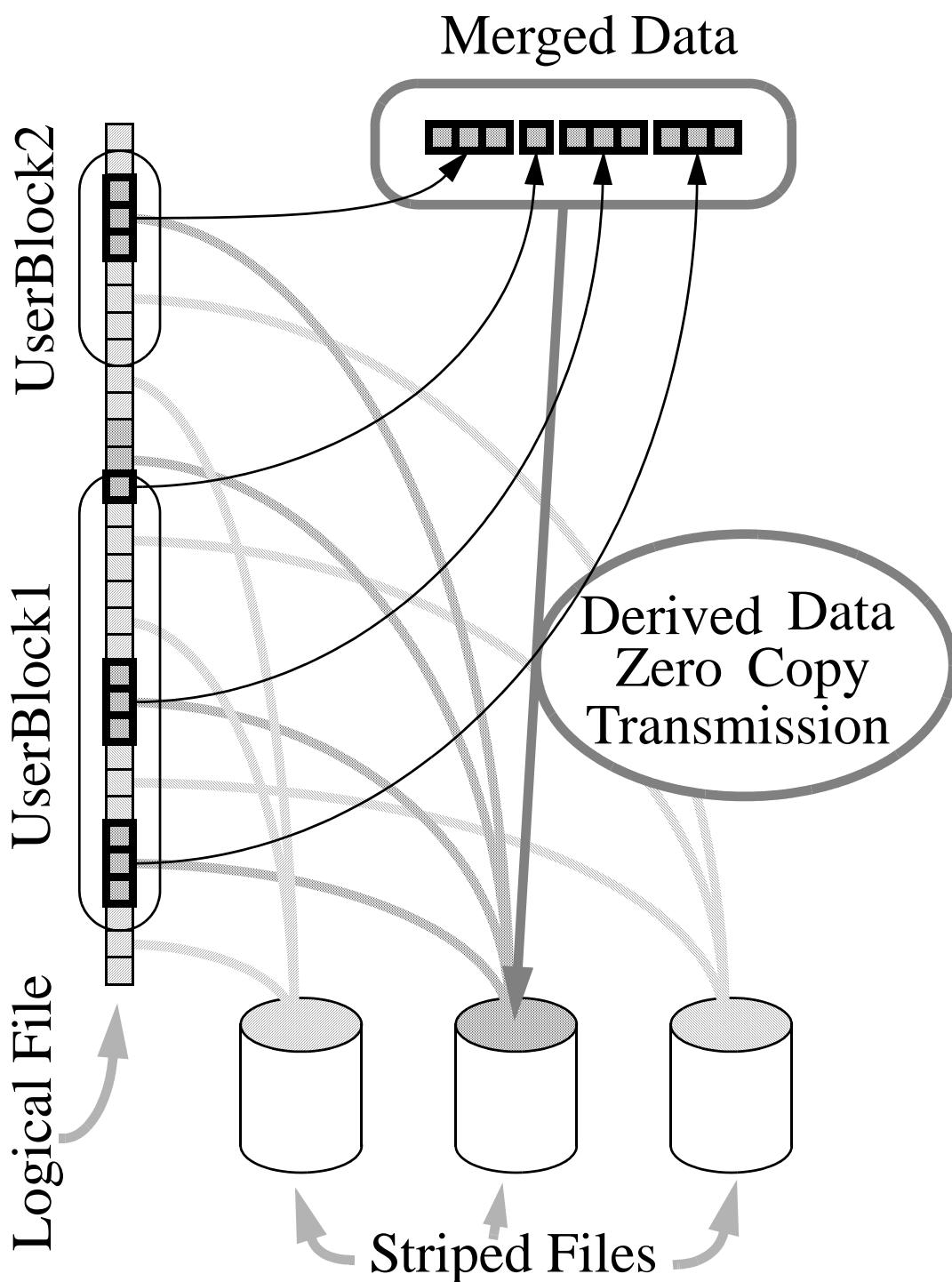
<code>MPI_File_open</code>	<code>MPI_File_write_all</code>
<code>MPI_File_set_view</code>	<code>MPI_File_read_all</code>
<code>MPI_File_write</code>	<code>MPI_File_write_at_all</code>
<code>MPI_File_read</code>	<code>MPI_File_read_at_all</code>
<code>MPI_File_write_at</code>	<code>MPI_File_close</code>
<code>MPI_File_read_at</code>	<code>MPI_File_delete</code>

face.

## Network and Disk Optimisation

- Network optimisation for noncollective access
- Disk optimisation for noncollective access
- Collective access optimisation

## Network Transmission Optimisation



# Disk Access Optimisation

Fig. 07. Data fragmentation is resolved for network communication

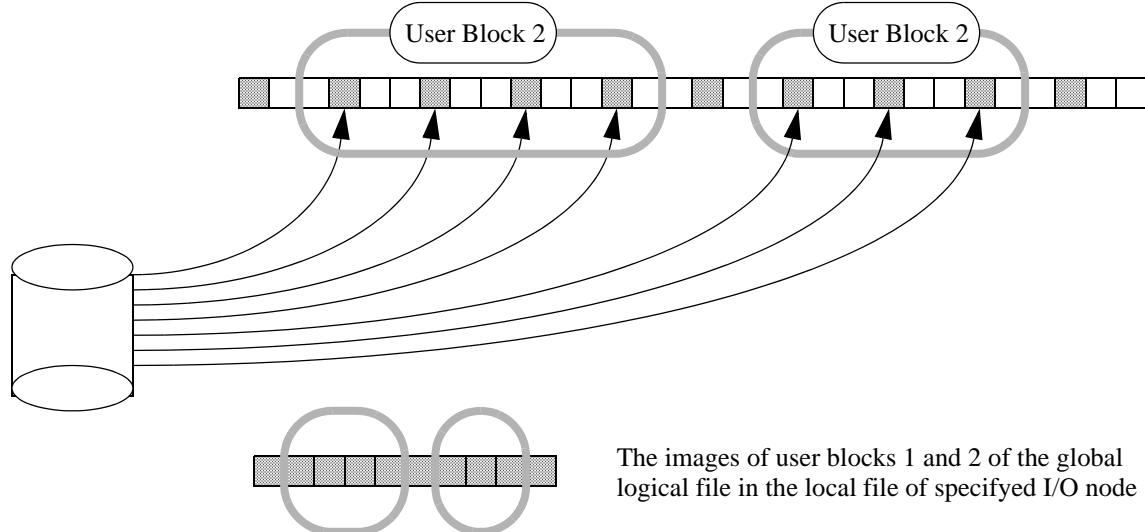


Fig. 08. Data fragmentation still exist at disk access

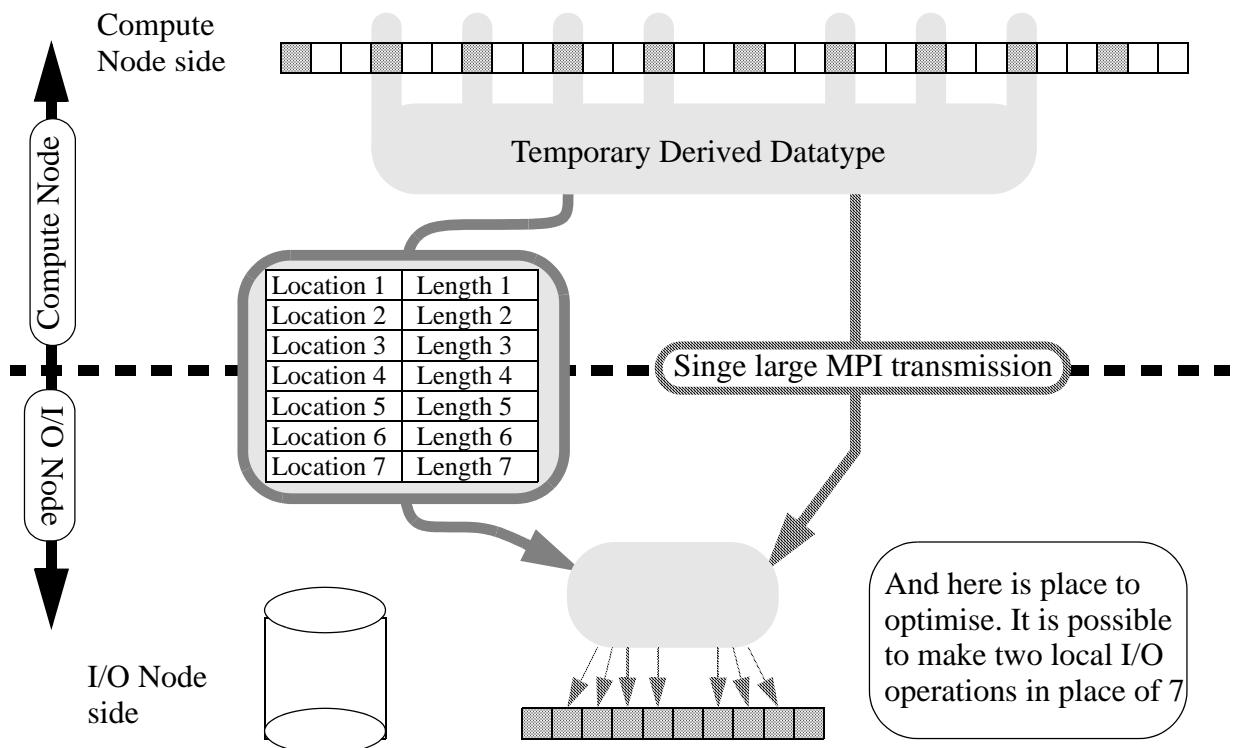


Fig. 09. Data fragmentation is resolved on disk level also

## Disk Access Optimisation

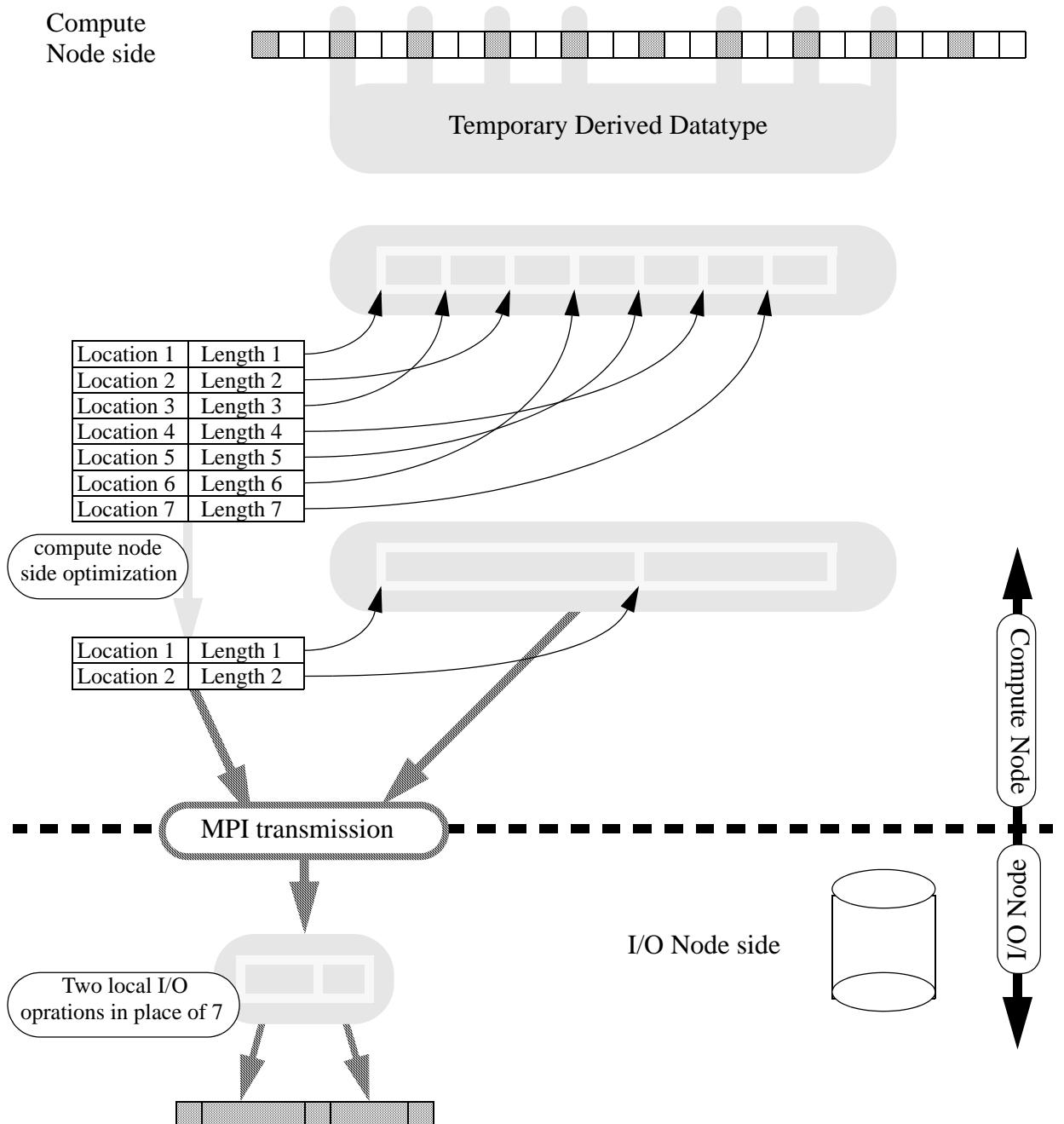


Fig. 10. Disk Access Optimisation of Collective Operations

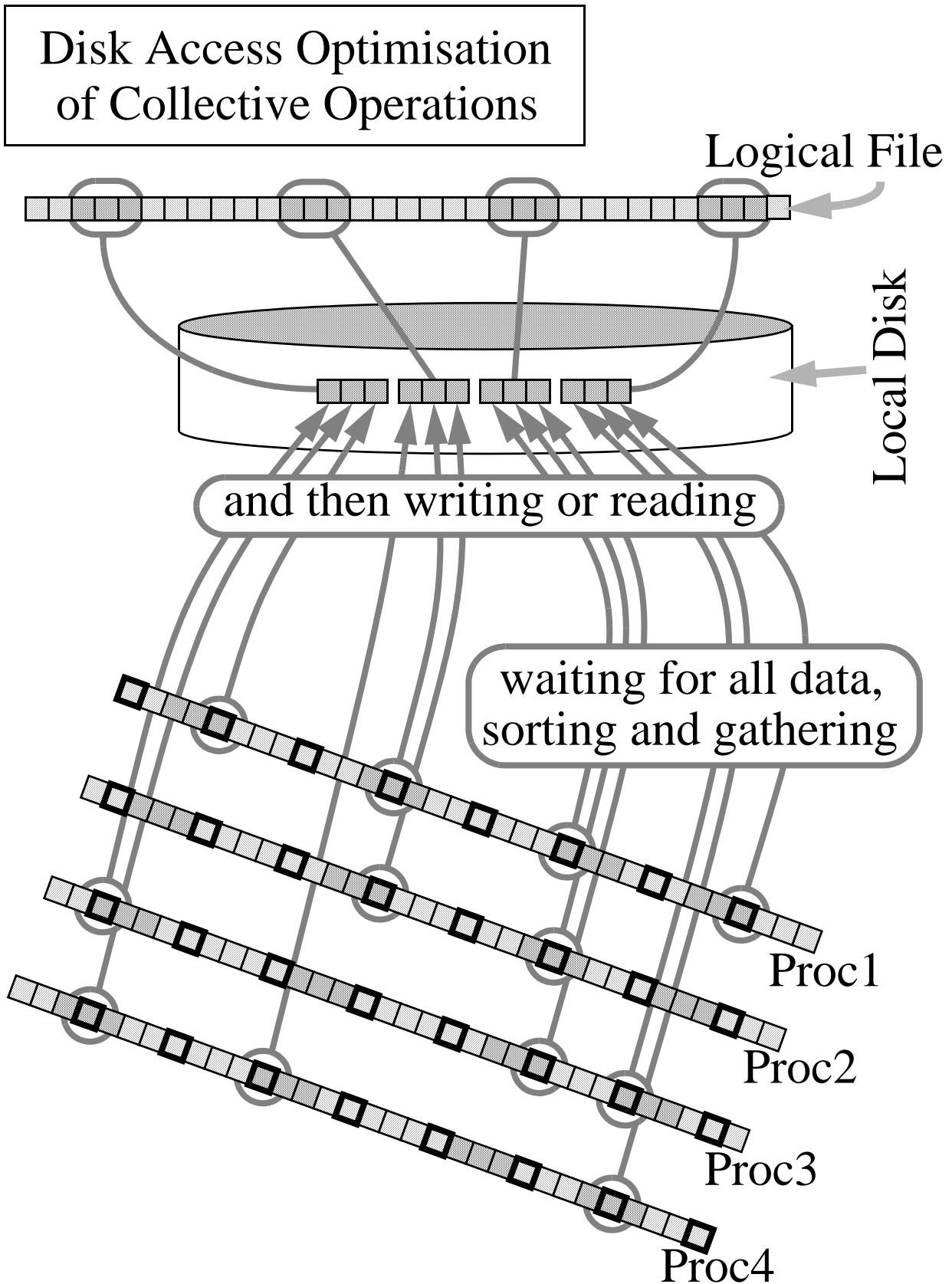
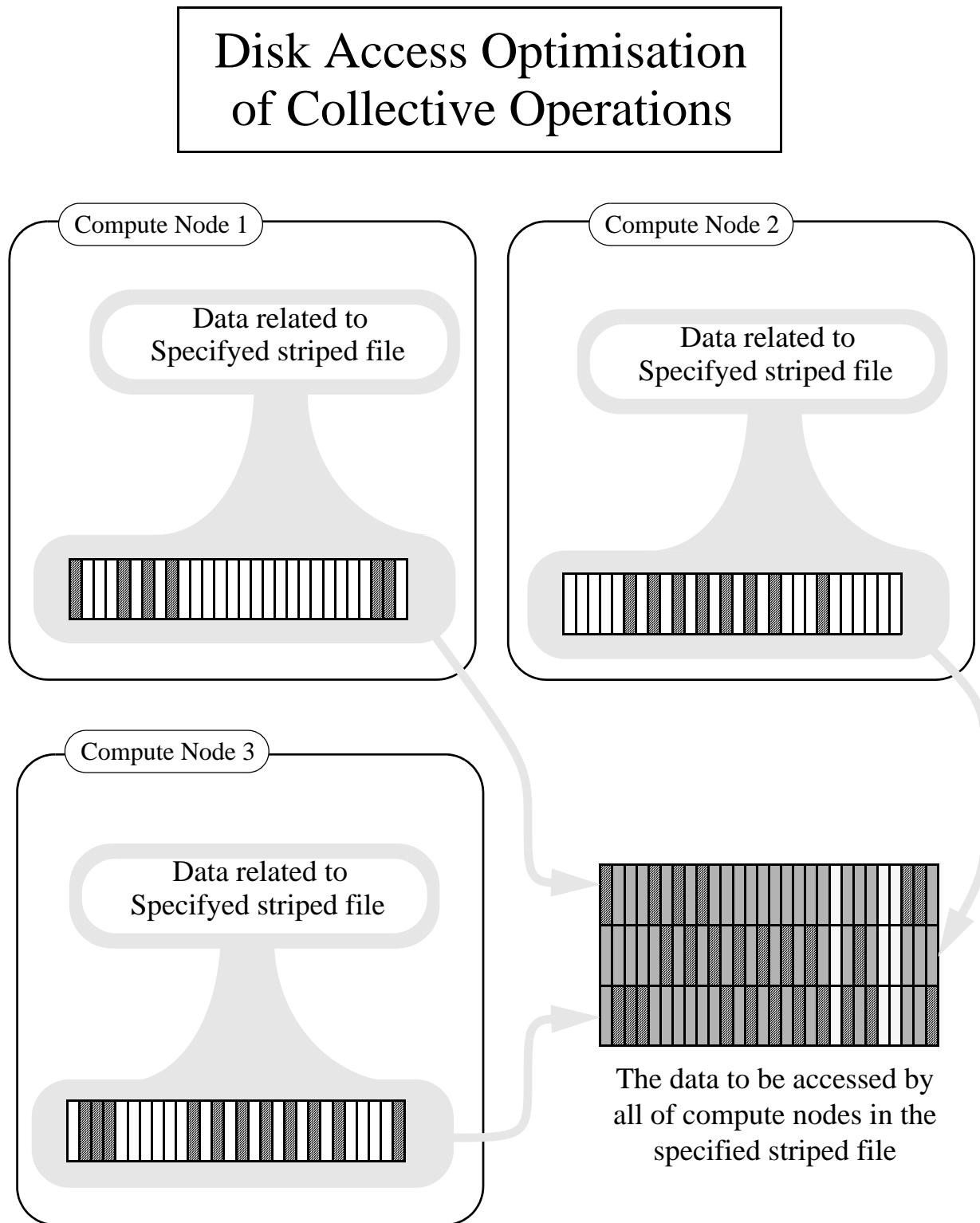


Fig. 11. Another example when disk access optimisation is required at collective



operations. Here each compute node tries to access some part of logical file, but in the picture is shown the translation of this part to the view of some specified striped file. For each of compute node the striped file is same.

Fig. 12.

## SFIO Software Architecture

- Encapsulation over MPI
- Functionality
- Data organisation and optimisation

Fig. 13. Encapsulation over MPI

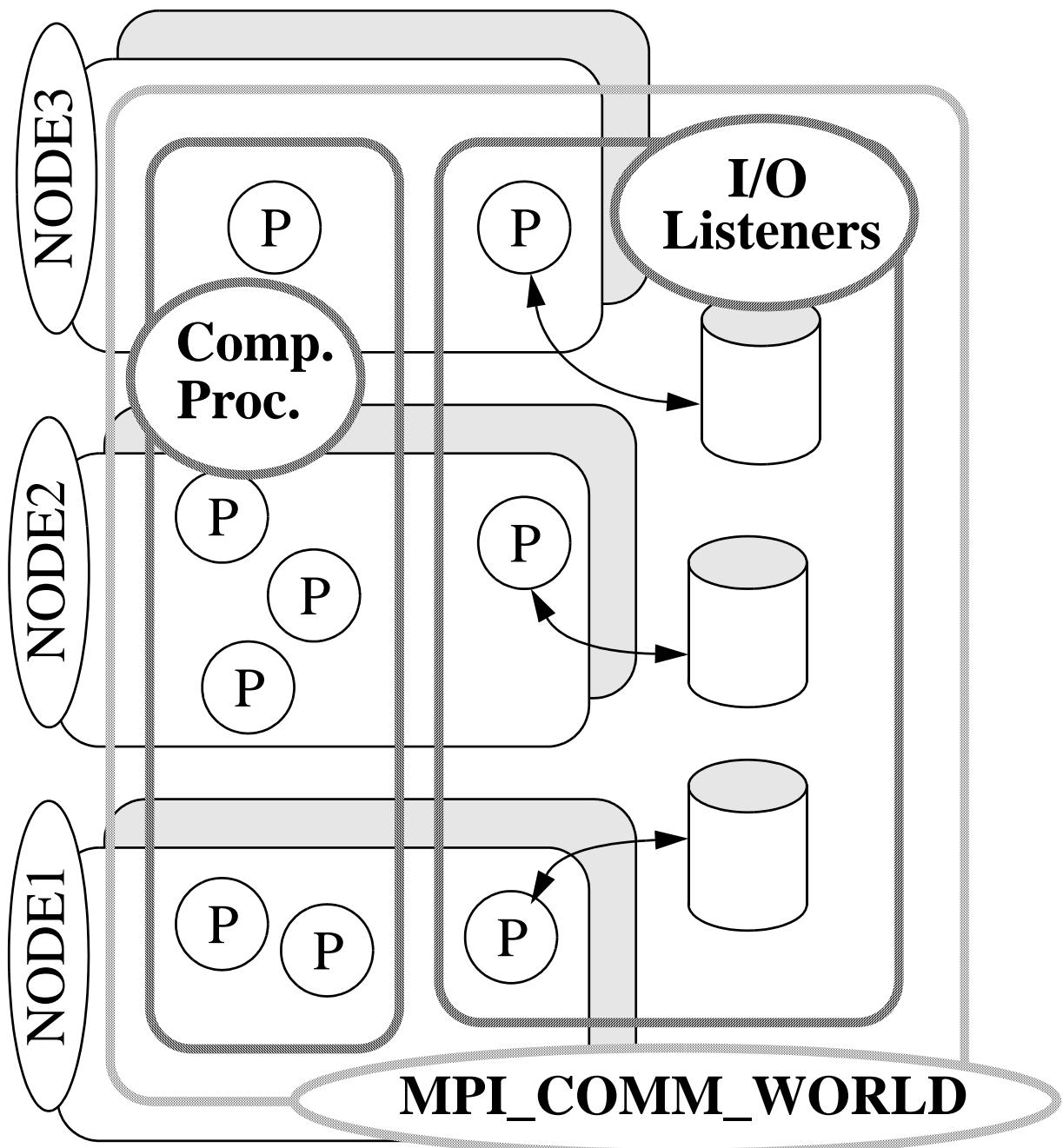
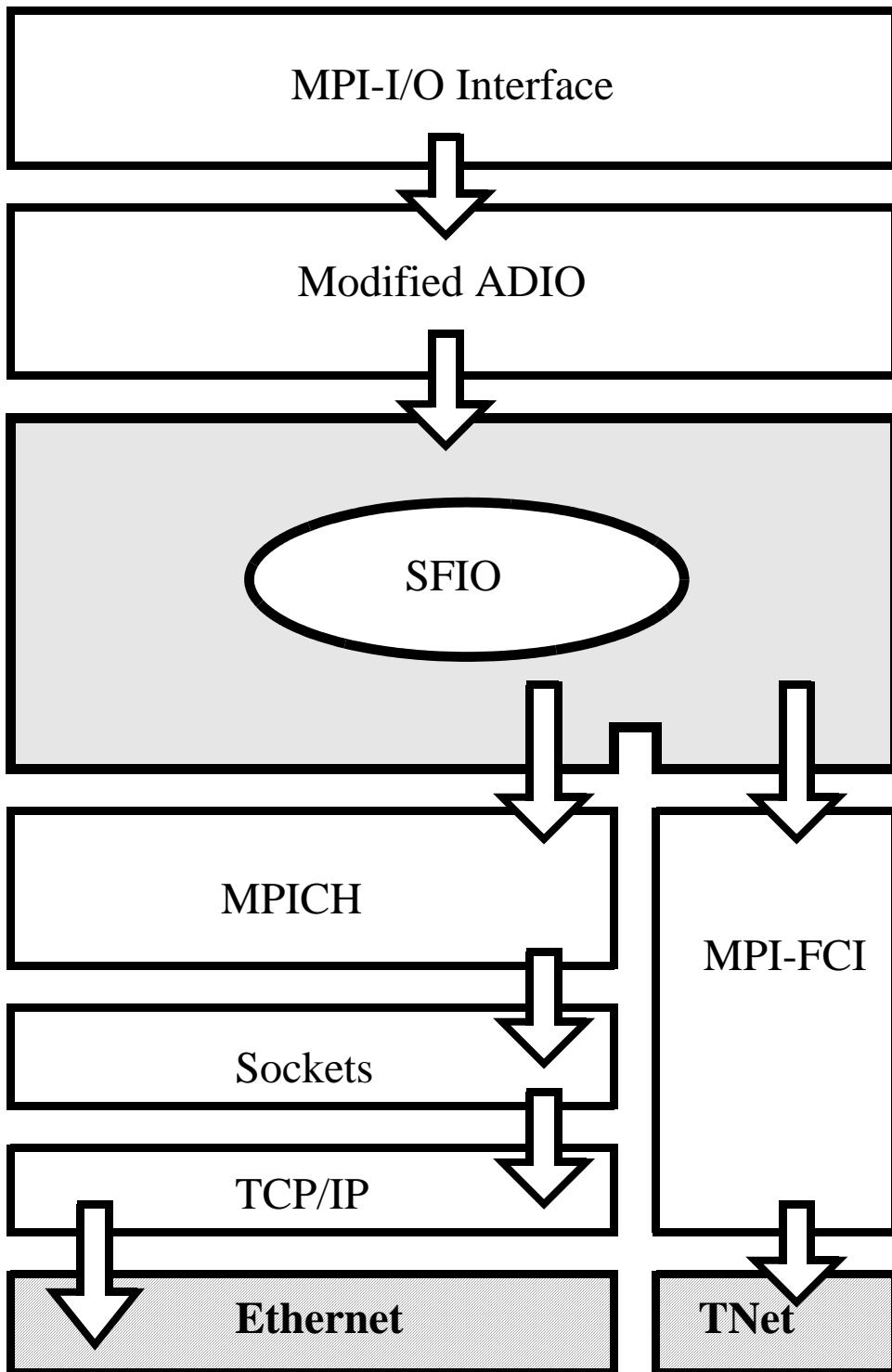


Fig. 14. Functionality. SFIO is implemented and tested with Digital Unix



(MPICH, FCI) and Intel Windos NT (MPICH).

Fig. 15. Functionality, data organisation and optimisation

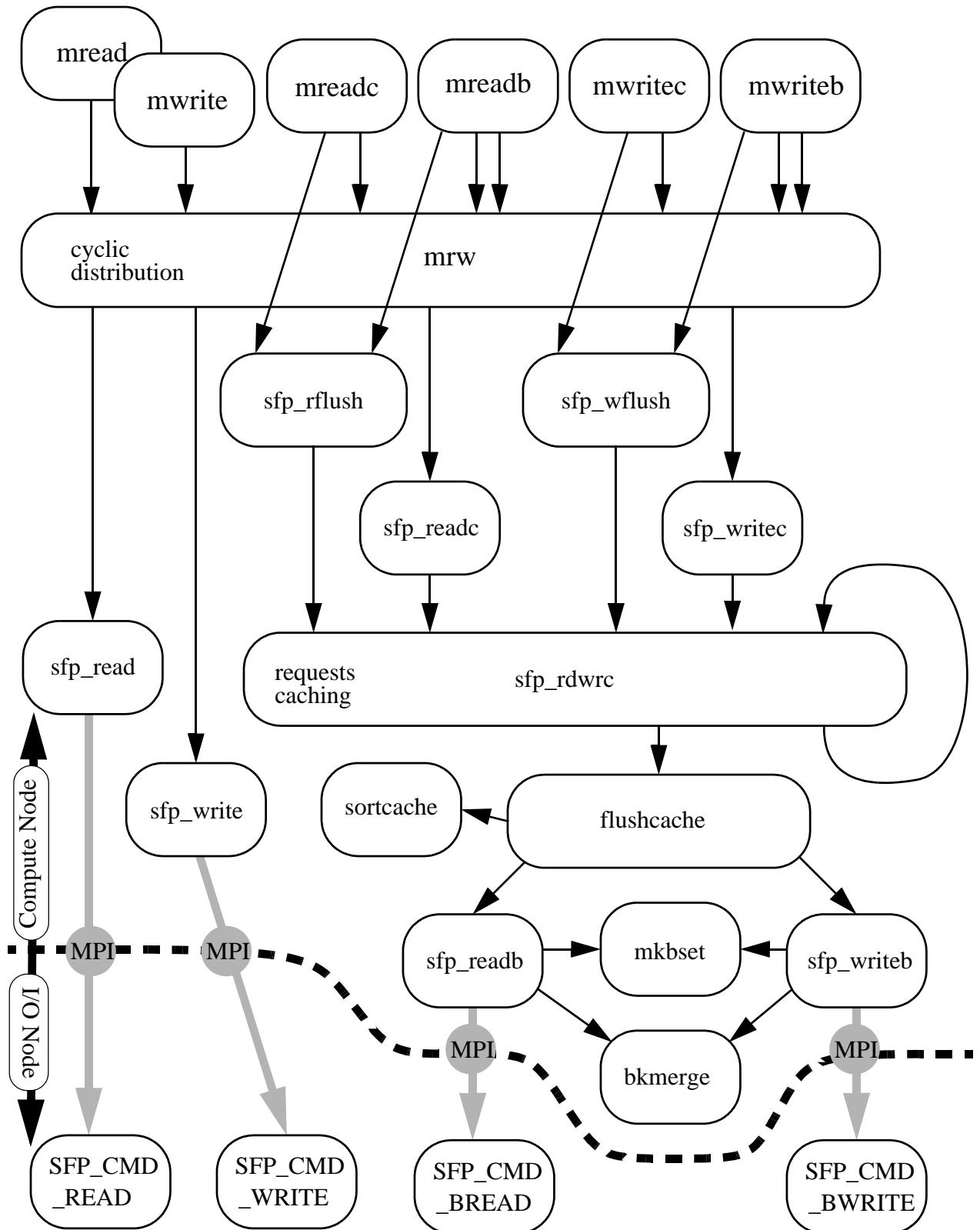


Fig. 16.

## SFIO Interface

- Single Block Access Interface
- Multiple Block Access Interface

Fig. 17. If there is one compute node

## Single Block Access Interface

```
#include "/usr/p5/gabriely/mpi/lib/mio.h"

int _main(int argc, char *argv[])
{
    char buf[]="ABCDEFGHIJKLMNPQRSTUVWXYZ";
    MFILE *f;

    f=mopen( "t0-p1.epfl.ch,/tmp/aa;t0-p2.epfl.ch,/tmp/aa" , 10 );
    mwrite( f , 0 , buf , 26 );
    mclose(f);
    return 0;
}
```

The diagram illustrates the mapping of logical file operations to striped files. It shows the following components and their relationships:

- Names of Striped Files:** Contains the string "t0-p1.epfl.ch,/tmp/aa;t0-p2.epfl.ch,/tmp/aa".
- Offset in Logical File:** Contains the value "0".
- Stripe Unit Size:** Contains the value "10".

Arrows indicate the flow from the logical file operation parameters in the code to these mapped values.

Fig. 18. If there are more than one compute nodes

## Single Block Access Interface more than one compute processes

```
#include "/usr/p5/gabriely/mpi/lib/mio.h"
int _main(int argc, char *argv[])
{
    char buf[]="abcdefghijklmnopqrstuvwxyz";
    MFILE *f;
    int rank;
    MPI_Comm_rank(_MPI_COMM_WORLD,&rank);

    f=mopen( "t0-p1.epfl.ch,/tmp/aa;t0-p2.epfl.ch,/tmp/aa" , 10 );
    mwrite(f, rank*26 , buf , 26 );
    mclose(f);
    printf("rank=%d\n",rank);
    return 0;
}
```

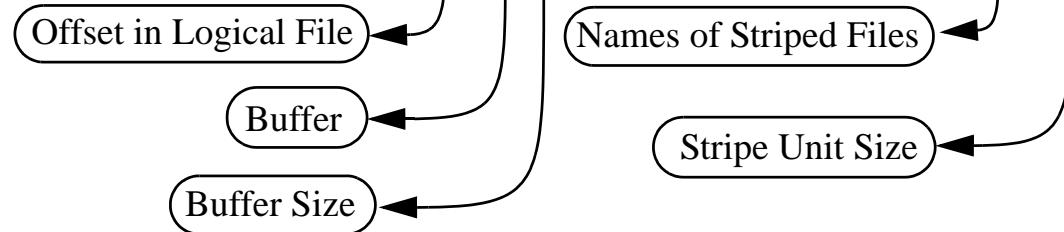


Fig. 19.

## Multiple Block Access Interface

```
#include "/usr/p5/gabriely/mpi/lib/mio.h"
int _main(int argc, char *argv[])
{
    char buf1[]="abcdefghijklmnopqrstuvwxyz";
    char buf2[]="ABCDEFGHIJKLMNOPQRSTUVWXYZ";
    char buf3[]="0123456789";
    char* Buf[]={buf1,buf2,buf3};
    long Offset[]={0,26,52};
    unsigned Count[]={26,26,10};
    MFILE *f;

    f=mopen("t0-p1.epfl.ch,/tmp/aa;t0-p2.epfl.ch,/tmp/aa",10);
```

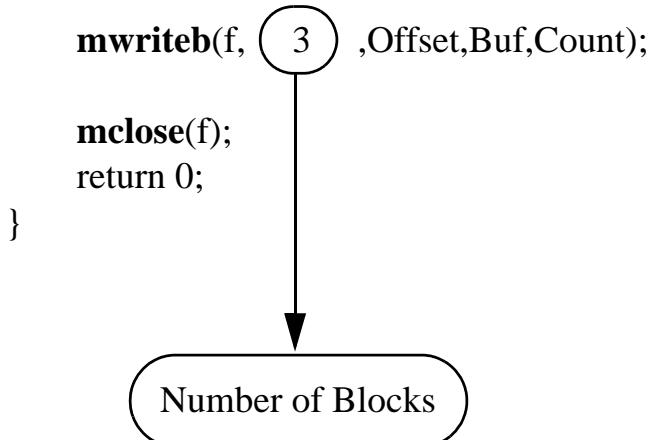


Fig. 20.

## Performance Measurements

Data Access Performance on Windows NT cluster (4 I/O nodes). Effect of Optimisation

Data Access Performance on Swiss-Tx SFIO over MPICH with 100Mbps ethernet HUB (7 I/O nodes). Effect of Optimisation

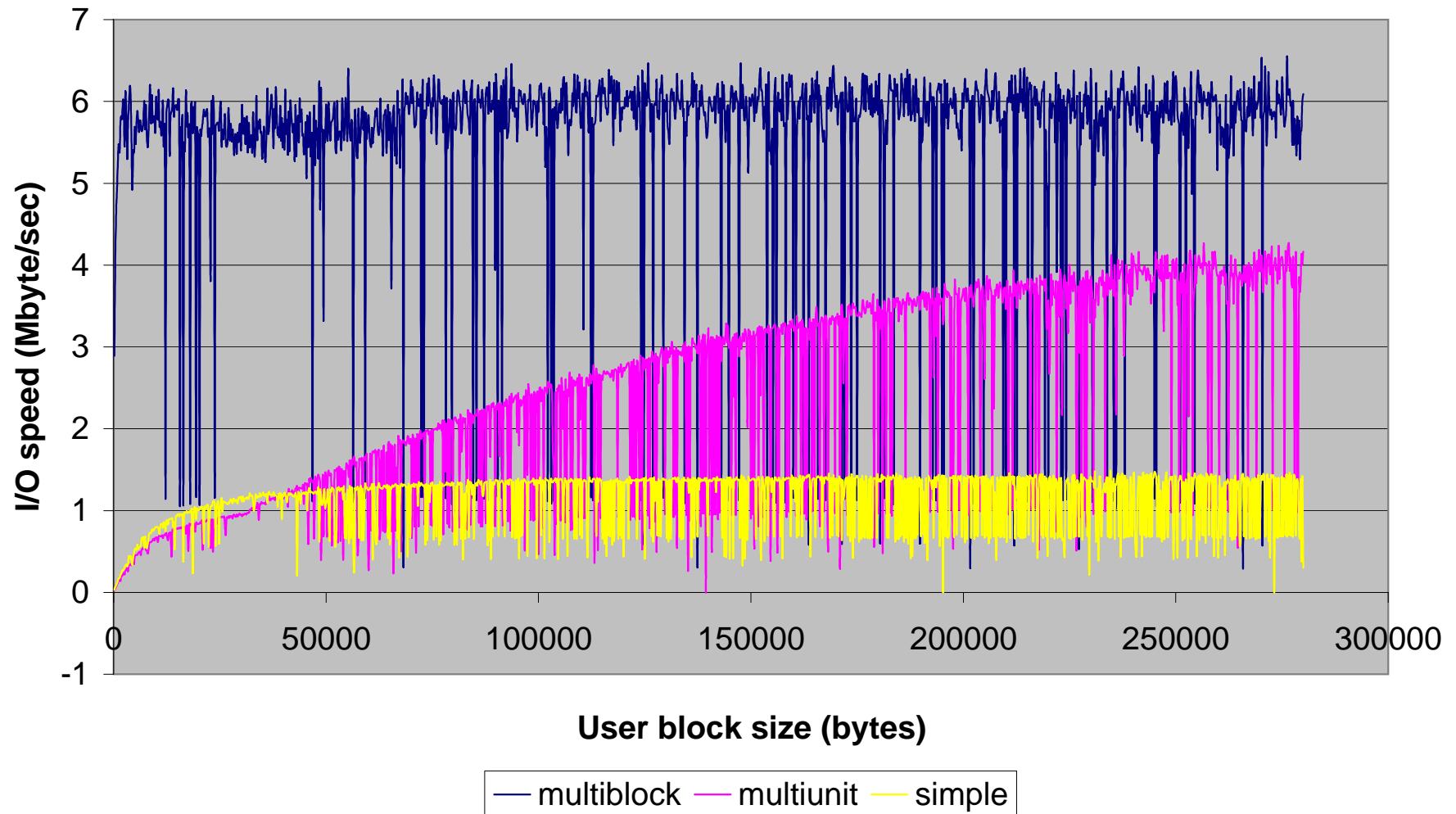
Data Access throughput dependance from stripe unit size (susz) and user block size (bksz) on Windows NT cluster (4 I/O nodes)

Data Access throughput dependance from stripe unit size (susz) and user block size (bksz) on Swiss-Tx SFIO over MPICH with 100Mbps ethernet HUB (7 I/O nodes).

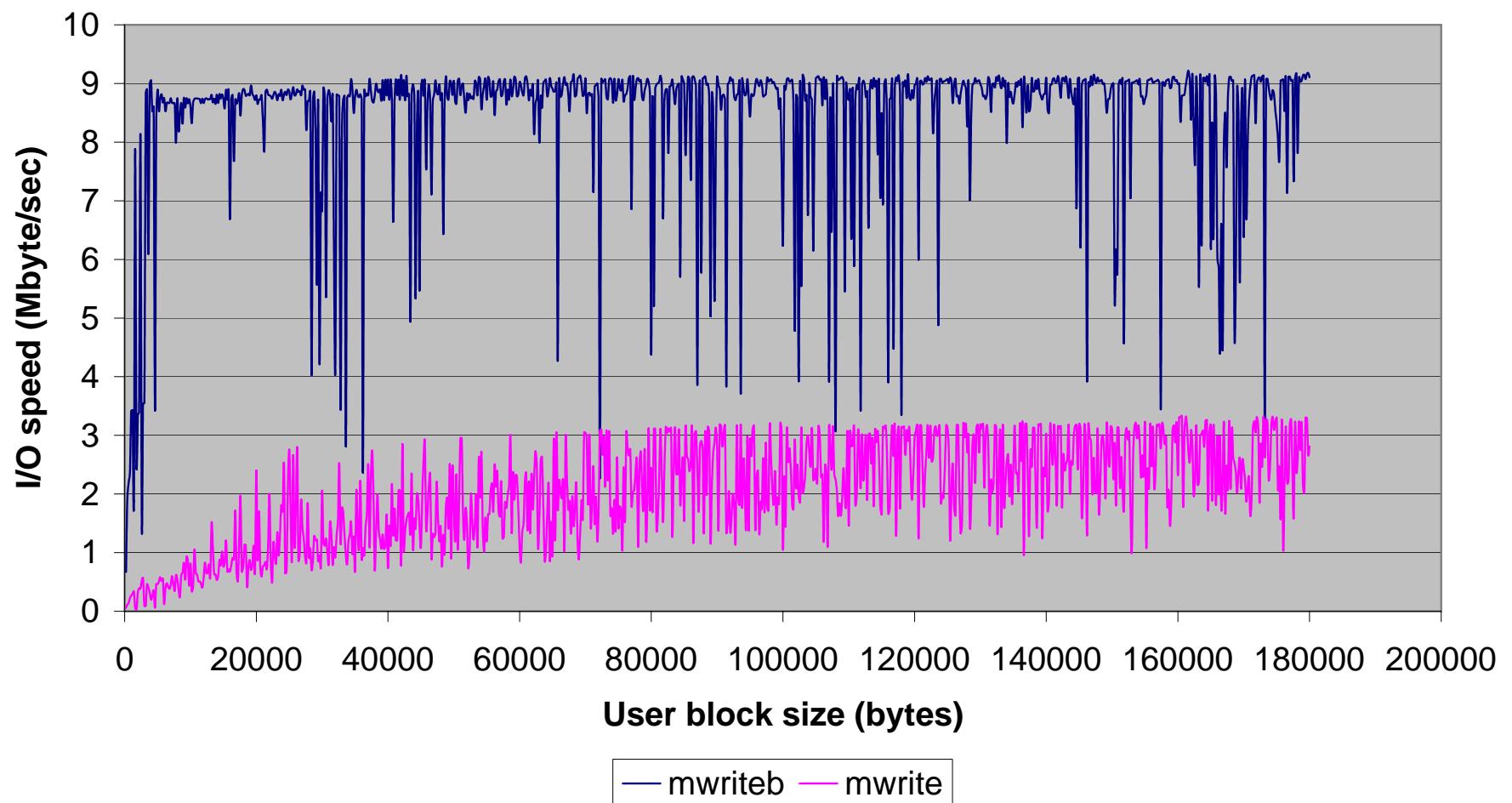
Performance measurements on Swiss-Tx SFIO over MPICH with GigaEthernet Switch (4 I/O nodes) for different number of concurrently reading/writing compute processes.

Performance measurements on Swiss-Tx SFIO over MPI-FCI with TNET Crossbar Switch (4 I/O nodes). Effect of Optimisation

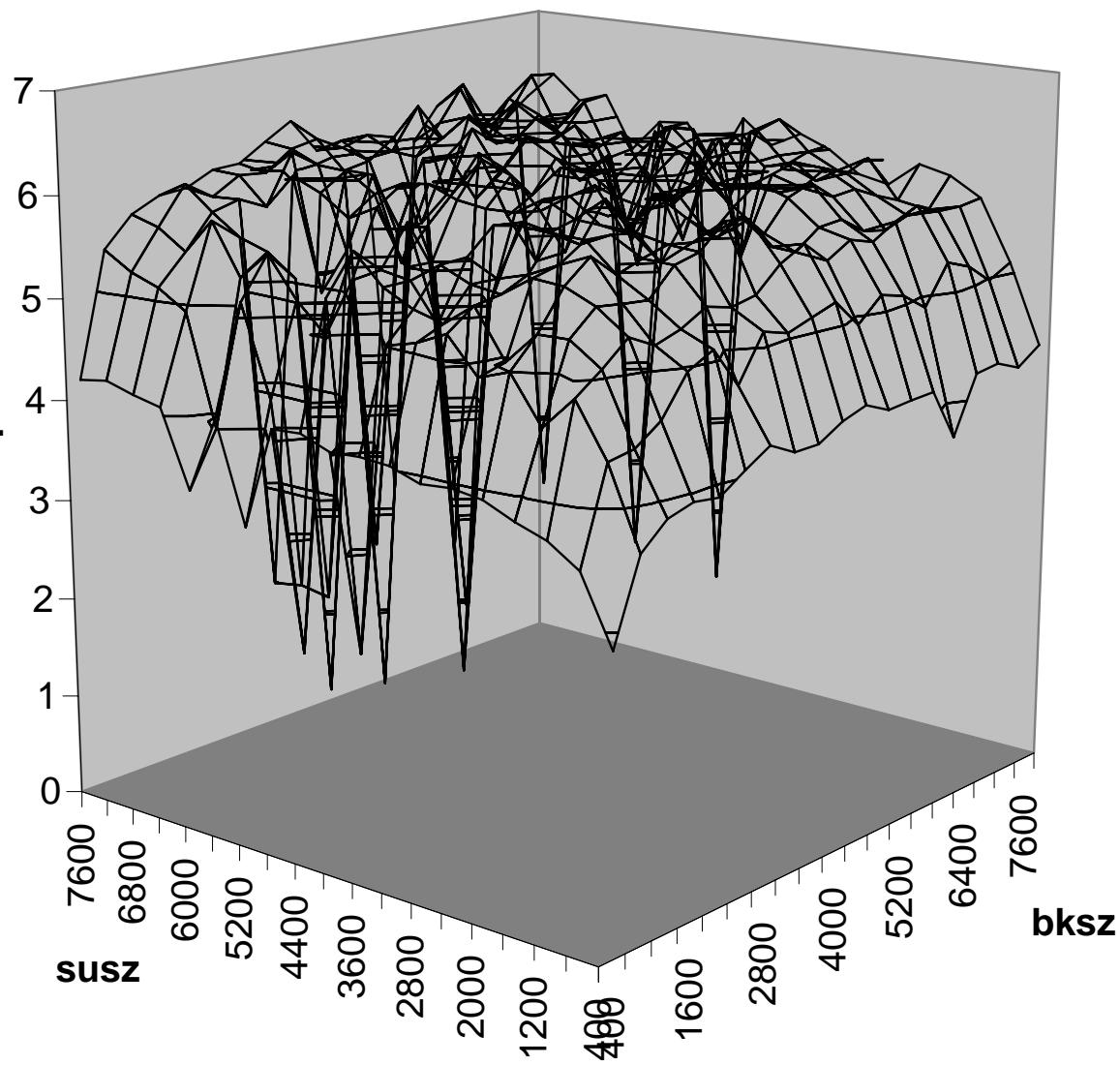
**Fig. 21. Multiblock interface (WinNT)**



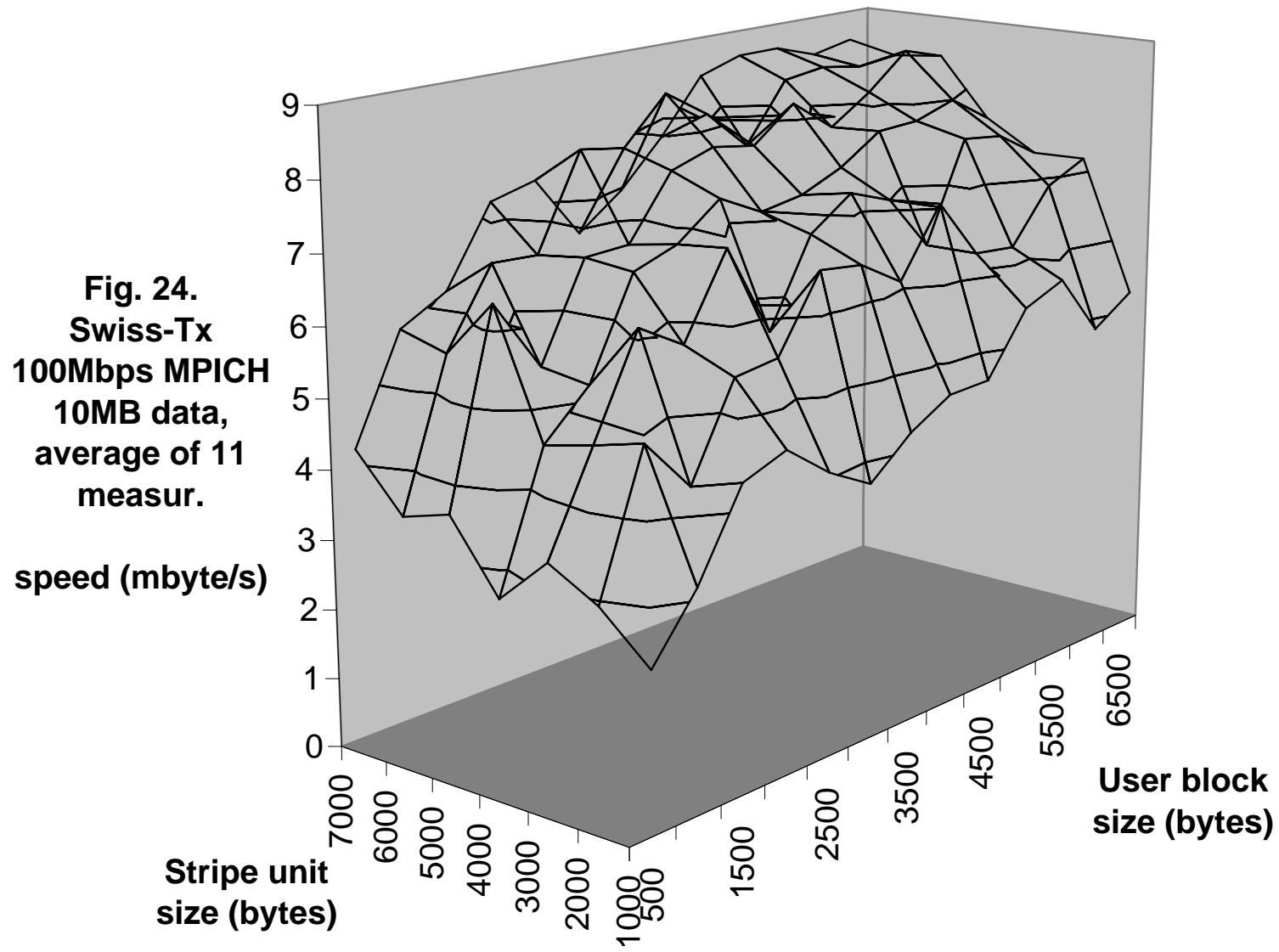
**Fig. 22. Multiblock user interface (Sw-tx, stripe unit = 1005 bytes)**



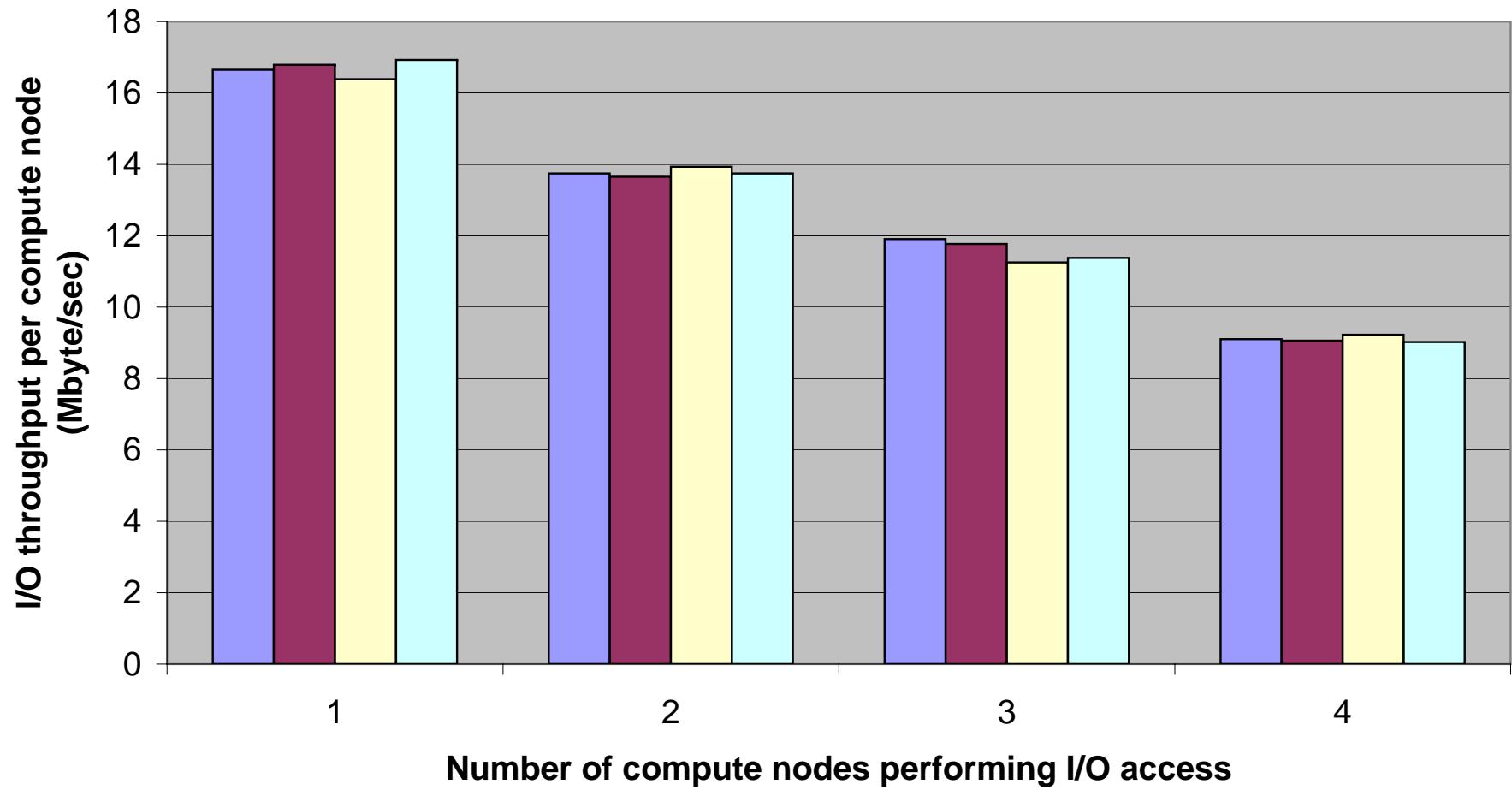
**Fig. 23.**  
**wnt**



**Fig. 24.**  
**Swiss-Tx**  
**100Mbps MPICH**  
**10MB data,**  
**average of 11**  
**measur.**



**Fig. 25. mwrite 4 I/O nodes Swiss-tx Gigabit ethernet Crossbar switch**



**Fig. 26. TNET 3 I/O nodes 1 compute node 660Mbyte**

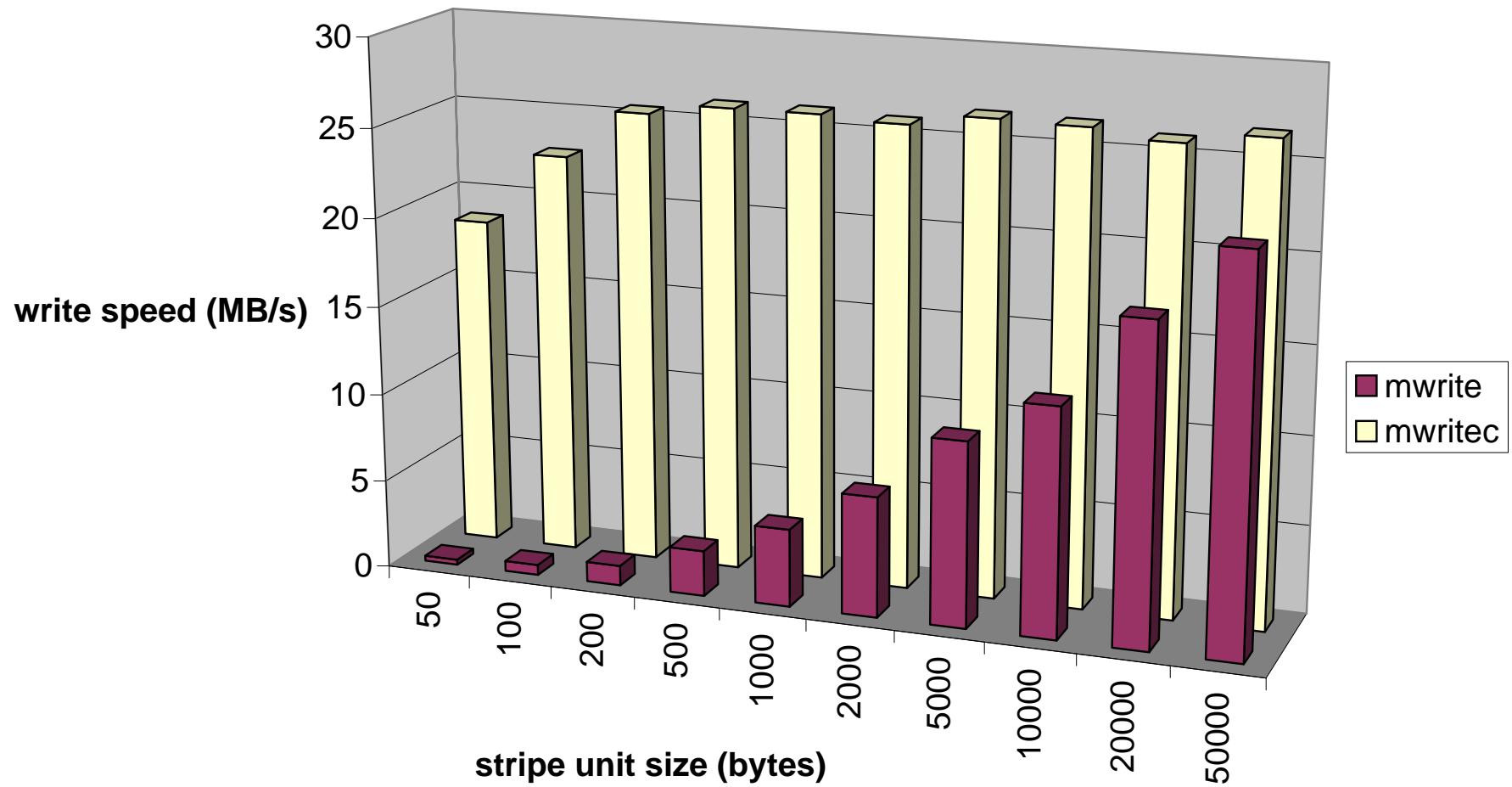


Fig. 27.

## Integration into MPI-II I/O

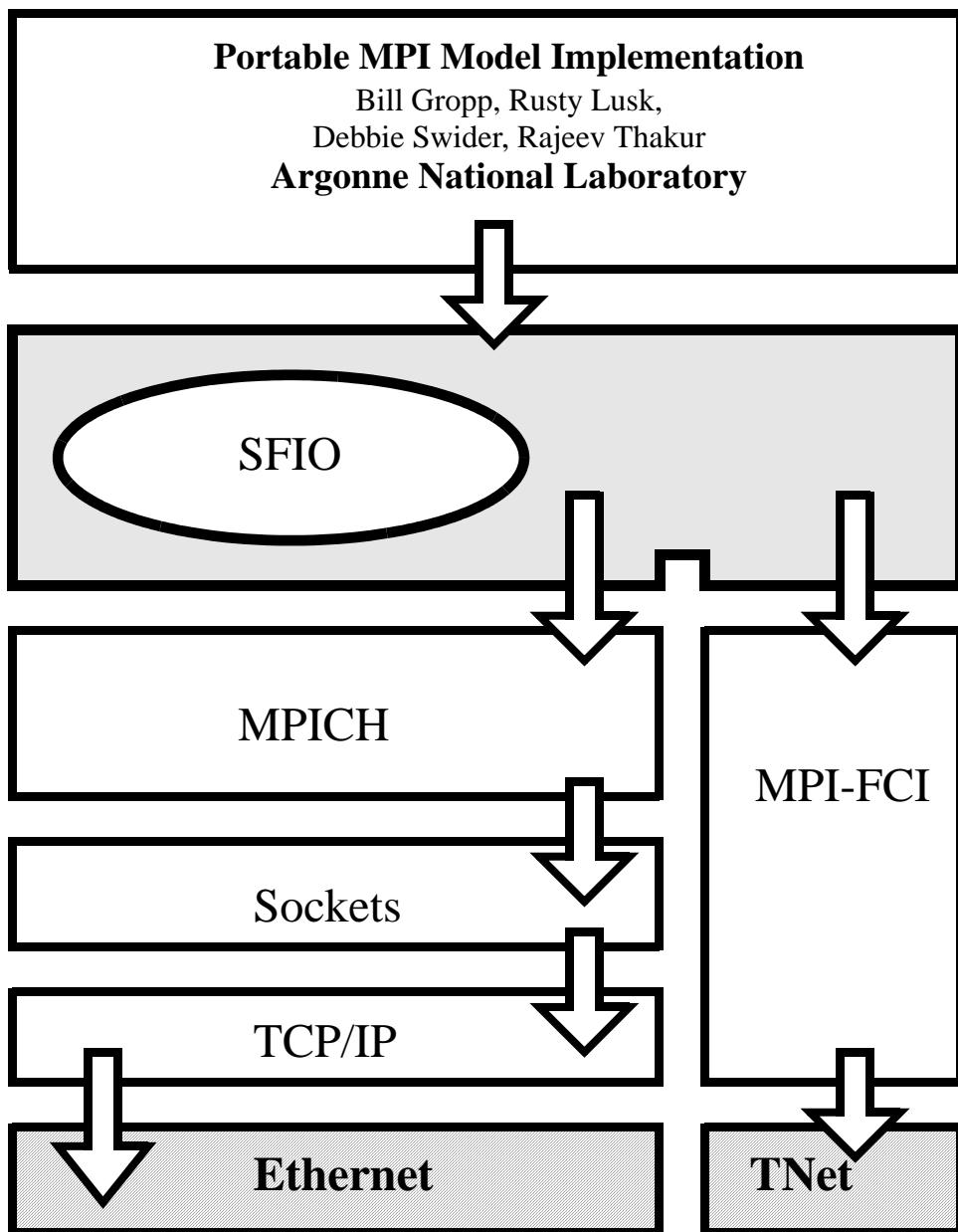


Fig. 28.

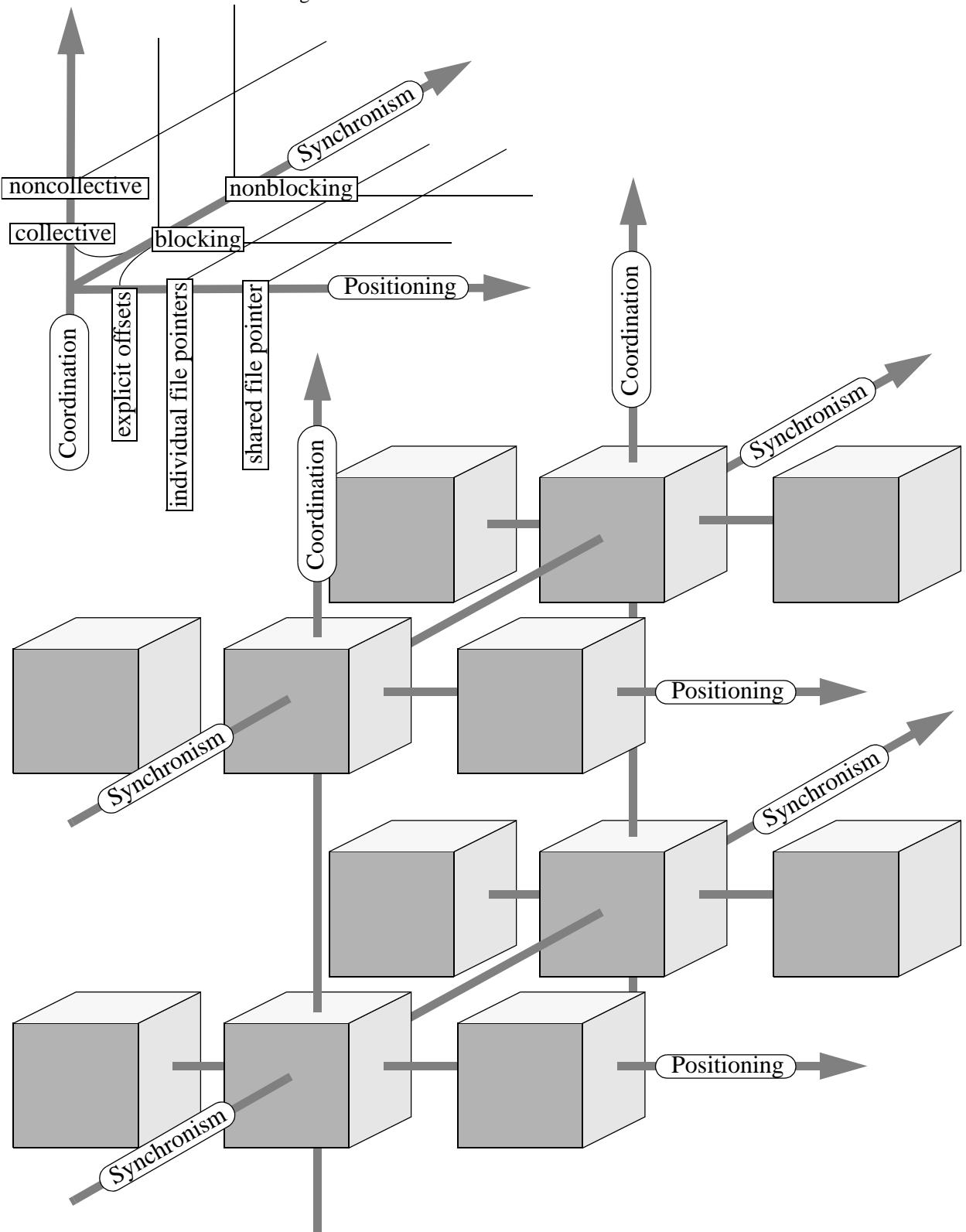


Fig. 29.

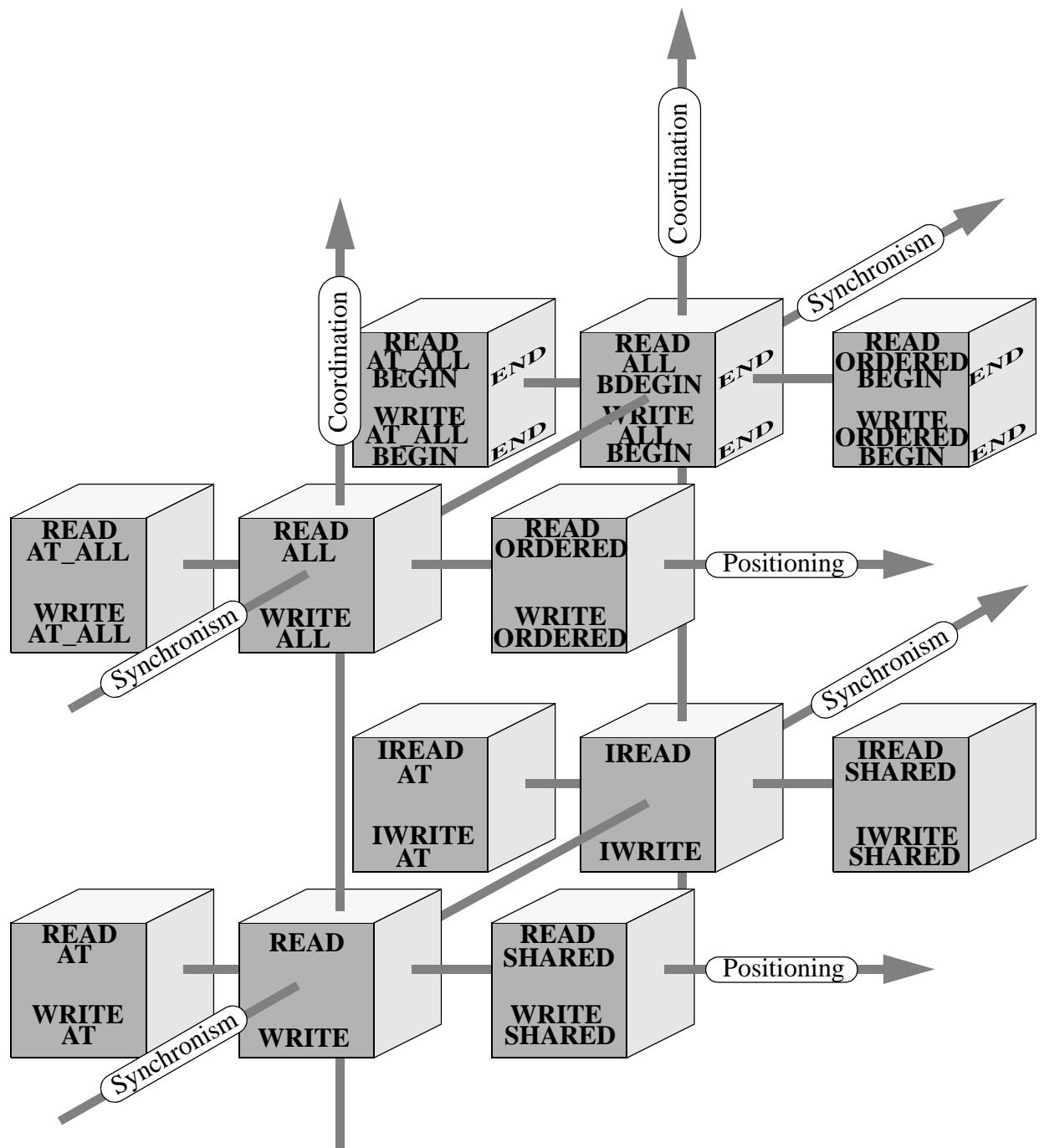
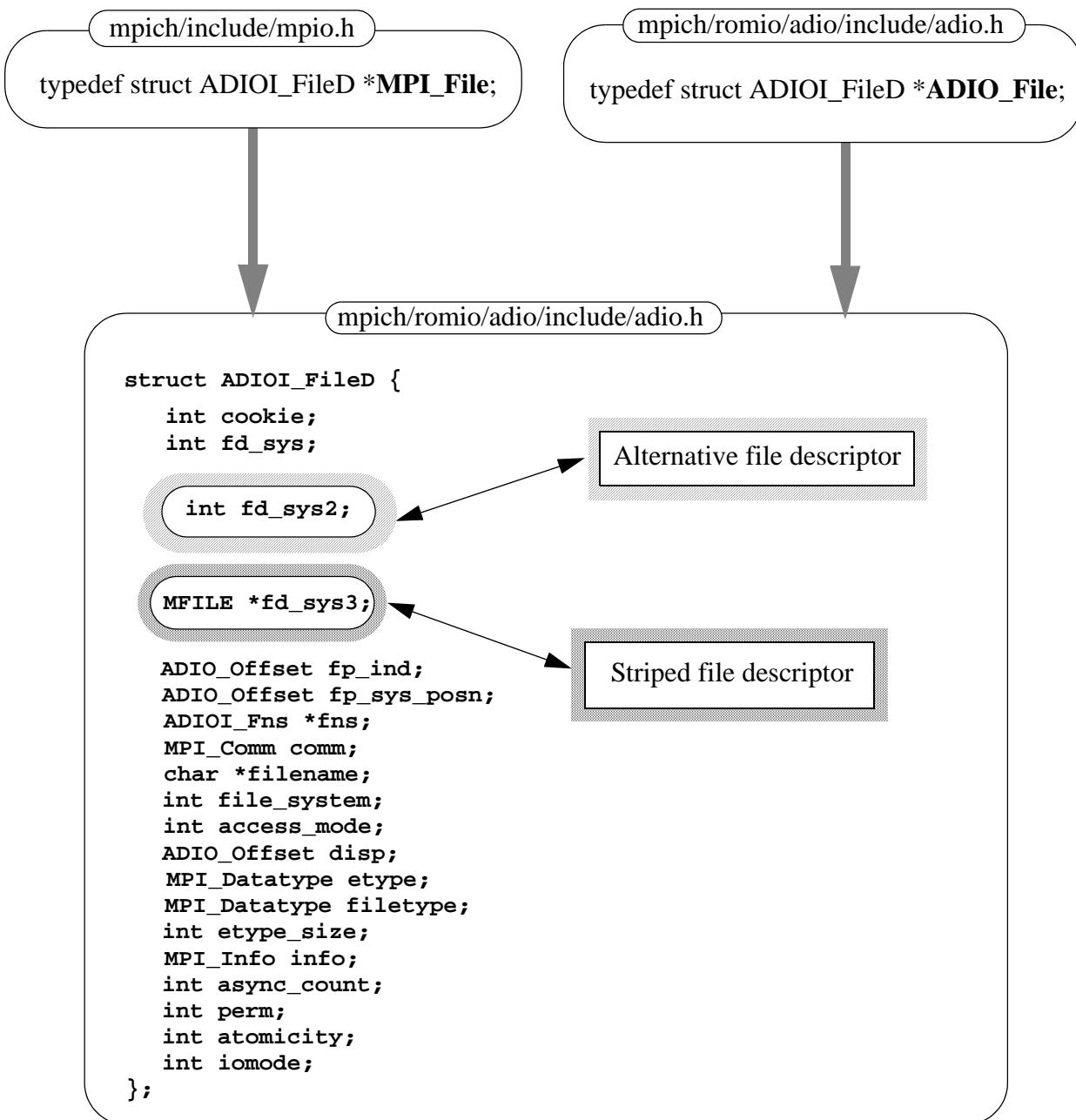


Fig. 30.



- We modify `MPI_File_open` operation

- We modify `MPI_File_close` operation

Fig. 31.

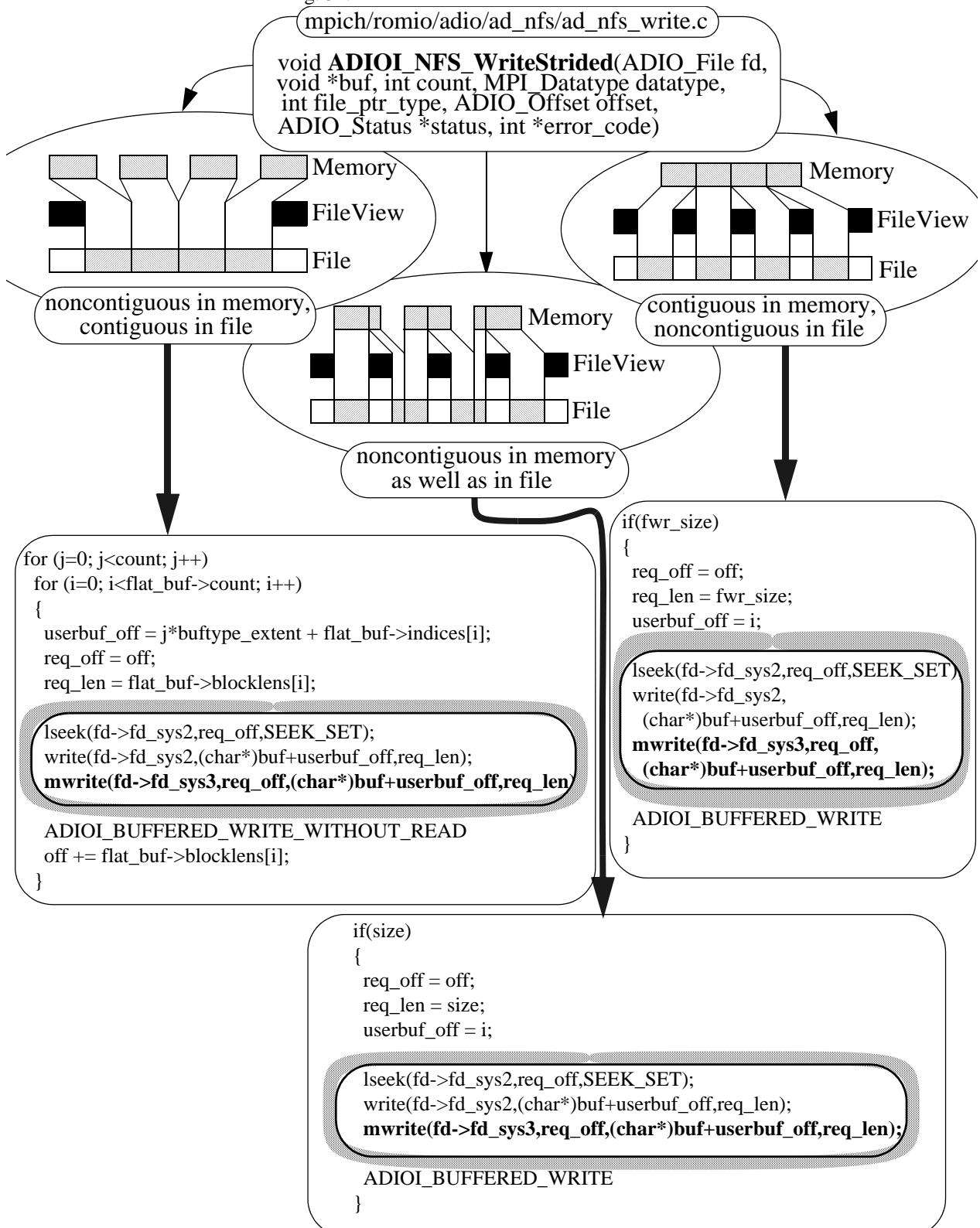


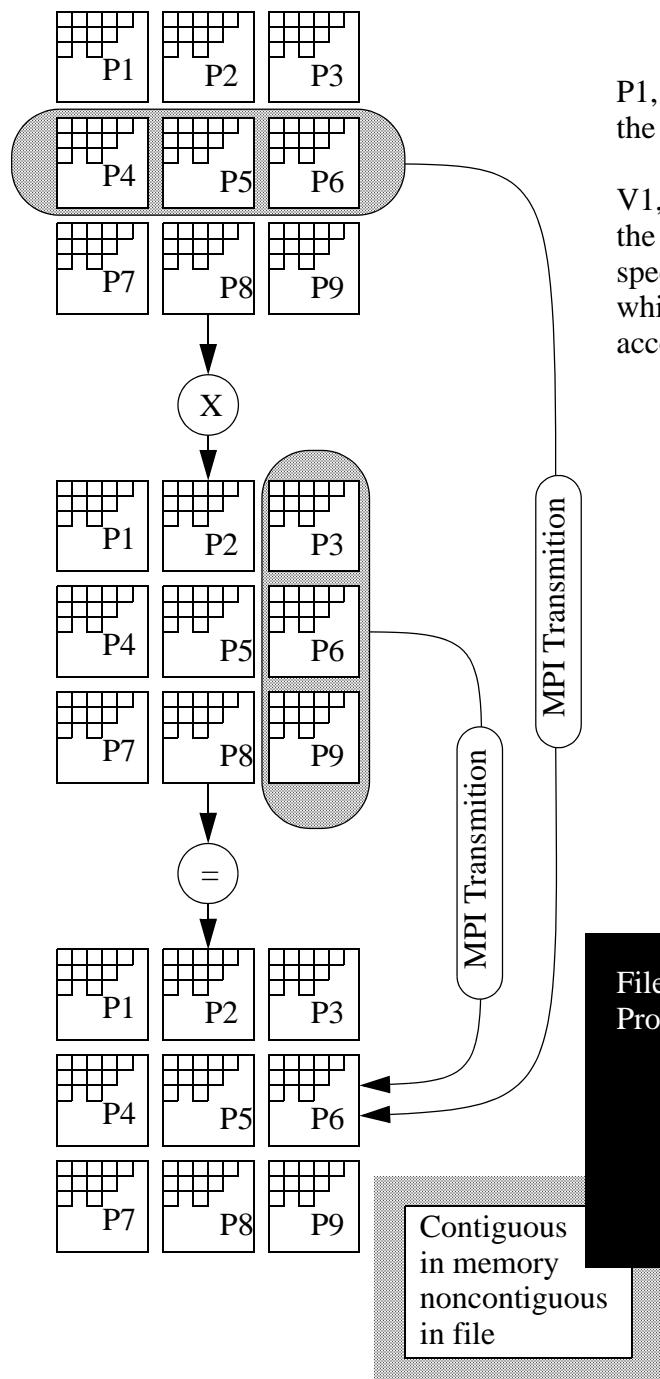
Fig. 32.

## Conclusion

- This is a cheap way of obtaining high performance I/O

Fig. 33.

Data in memory of 9 processes.  
Each process keep only 1/9-th of matrix



Three files of three matrixes. Files are accessible by all of processes, but for each process is visible only spec. part.

P1, P2, ... P9 are the set of processes

V1, V2, ... V9 are the set of views, which specify for each process which part of file is accessible.

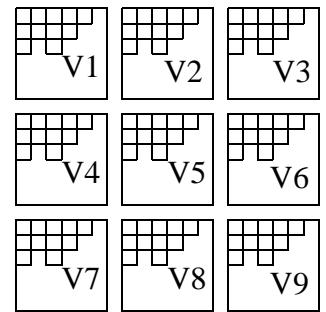
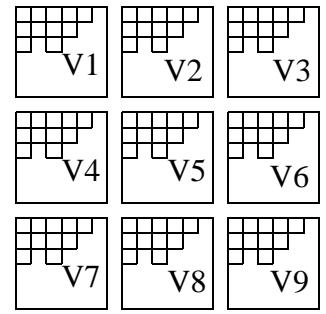
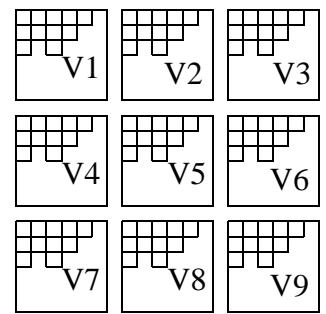


Fig. 34.

